

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Bin-Shing Chen

Group Art Unit: 2811

U.S. Application No.: 09/769,576

Examiner: Tran, Thien F.

Filing Date:

January 25, 2001

Attorney Docket No.: DEEP-1001US

For:

METHOD FOR MANUFACTURING SPLIT-GATE EEPROM MEMORY CELL AND STRUCTURE FORMED THEREBY

## REQUEST FOR APPOINTMENT OF ASSOCIATE POWER OF ATTORNEY CHANGE OF CORRESPONDENCE ADDRESS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Enclosed please find a copy of the Request For Appointment of Associate Power of Attorney and Change Of Correspondence Address, which was previously filed on August 7, 2003, and October 14, 2003; and a copy of the Request for Customer Data Change filed herewith.

Applicant notes that the correspondence address for this file has not yet been changed. Please note these changes and send all future correspondence to the address listed in this communication.

### Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this document, along with any paper referred to as being attached or enclosed, is being deposited with the U.S. Postal Service as first class mail under 37 CFR 1.8 on January 9, 2004 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Name of person mailing correspondence

Signature of person mailing correspond

U.S. Application No. 09/769,576 Request for Appointment of Associate Power of Attorney Page 2

Date: //a/2004

Kevin J Dunleavy Registration No. 32,0

Respectfully submitted,

KNOBLE & YOSHIDA, LLC Customer Number 21,302 Eight Penn Center, Suite 1350 1628 John F. Kennedy Blvd. Philadelphia, PA 19103

Telephone: (215) 599-0600 Facsimile: (215) 599-0601

Email: kjdunleavy@patentwise.com

# JAN 1 2 7004 PB

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re-application of: Bin-Shing Chen

Group Art Unit: 2811

. U.S. Application No.: 09/769,576

Examiner: Tran, Thien F.

Filing Date: January 25, 2001

Atty. Docket No.: DEEP-1001US

For:

METHOD FOR MANUFACTURING SPLIT-GATE EEPROM

MEMORY CELL AND STRUCTURE FORMED THEREBY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

### REQUEST FOR CUSTOMER NUMBER DATA CHANGE

Sir:

Please change the Customer Number Data Change for the abovementioned matter to associate it with Customer No. 21302.

The new Address for correspondence is now:

Kevin J. Dunleavy, Esq. Registration No. 32,024 KNOBLE & YOSHIDA, LLC Customer No. 21302 Eight Penn Center, Suite 1350 1628 John F. Kennedy Blvd. Philadelphia, PA 19103 Telephone: (215) 599-0600

Facsimile: (215) 599-0601

E-mail: kjdunleavy@patentwise.com

JAN 15 2004

### **Certificate of Mailing Under 37 CFR 1.8**

I hereby certify that this document, along with any paper referred to as being attached or enclosed, is being deposited with the U.S. Postal Service as first class mail under 37 CFR 1.8 on <u>January 9, 2004</u> and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Name of person mailing correspondence

Signature of person mailing correspondence

U.S. Application No. 09/769,576 Request For Customer Number Data Change Page 2

Respectfully submitted

Kevin J. Dunleavy, Hsq. Registration No. 32,024

KNOBLE & YOSHIDA, LLC Customer No. 21302 Eight Penn Center, Suite 1350 1628 John F. Kennedy Blvd. Philadelphia, PA 19103

Telephone: (215) 599-0600 Facsimile: (215) 599-0601

E-mail: kjdunleavy@patentwise.com



### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

# ASSOCIATE POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS

Commissioner for Patents P.O. Box 150 Alexandria, VA 22313-1450

Sir:

RECEIVED

JAN 15 2004

TECHHOLOGY CENTER 2800

I hereby appoint KNOBLE & YOSHIDA, LLC, Kevin J. Dunleavy, Registration No. 32, 024, John L. Knoble, Registration No. 32,387; Ken I. Yoshida, Registration No. 37,009 and Maria M. Kourtakis, Registration No. 41,129 as an associate attorney to prosecute each application listed on the attached Schedule A and to prosecute all continuation applications and/or divisional applications and to transact all business in the Patent and Trademark Office connected with these applications.

Please send all future correspondence to:

Kevin J. Dunleavy, Esq. KNOBLE & YOSHIDA, LLC Eight Penn Center, Suite 1350 1628 John F. Kennedy Blvd. Philadelphia, PA 19103 Telephone: (215) 599-0600 Facsimile: (215) 599-0601

E-mail: kidunleavy@patentwise.com

Customer No. 21302

Moreover, please note that the Attorney docket number for these matters has been changed.

Date: 7/15/03

Respectfully submitted,

Keith A. Vogt

Registration No. 37,252

Niro Scavone Haller & Niro

181 W. Madison Street, Suite 4600

Chicago, IL 60602

(312) 236-0733

⋖	
$\equiv$	
$\preceq$	
$\Xi$	ľ
$\Xi$	
じ	
S	l

	Heat-Dissipating Device	Feb. 26, 2001 May 14, 2002	09/793,060/ 6,386,276	DEEP-1018US	3317
	Sheet Feeding Apparatus	Jan. 2, 2001/ Dec. 24, 2002	09/752,486/ 6,497,405	DEEP-1017US	3311
	Air-Flow Fan Guard	Jul. 12, 2000 / Feb. 25, 2003	09/614,941/ 6,524,067	DEEP-1016US	3264
	Multi-Level Spiral Inductor Structure Having High Inductance (L) and High Quality Factor (Q)	Oct. 4, 2000 Jul. 16, 2002	09/679,092/ 6,420,773	DEEP-1015US	3281
	Method for Molding Substance on Substrate and Device Formed Thereby	Jan. 11, 2002	10/045,791	DEEP-1014USDIV	3257DIV
in the second se	Method for Molding Substance on Substrate and Device Formed Thereby	Jun. 9, 2000	09/591,325	DEEP-1014US	3257
	Method For Manufacturing Improved Touch Pen and Device Formed Thereby	Jun. 9, 2000	09/590,948	DEEP-1013US	3256
	Data Processing System	Oct. 16, 2000	09/688,440	DEEP-1012US	3285
	Method of Implementing OSD Function and Device Thereof	Oct. 10, 2000	629,689,676	DEEP-1011US	3283
	Power Converter for Correcting Power Factor	Mar. 13, 2001/ Oct. 21, 2001	09/808,199/ 6,297,973	DEEP-1010US	3326
	Method for Suppressing Optical Proximity Effect Bias Within A Wafer	Jun. 21, 2000/ Dec. 24, 2002	09/598,828/ 6,497,982	DEEP-1009US	3261
1.5 mg/m/s.	Spine-indexing Device	Nov. 26, 2002	10/304,728	DEEP-1008US	3504
	Method and Circuit for Switching Source Modulation Frequency	Oct. 15, 2002	10/271,348	DEEP-1007US	3494
	Method For Manufacturing Split-Gate Eeprom Memory Cell And Structure Formed Theory	Jan. 25, 2001	975,697/60	DEEP-1001US	3308
	Title	Filing Date/ Issue Date	Application No./ Patent No.	New Docket No.	Old Docket No.
•		SCHEDULE A			
<i>\$</i>		·			

# SCHEDULE A (continued)

Title	Electroluminescence Element and Method for Fabricating the Same	Method for Developing Testing Program of Tester	Power Adapter and the Power Cord Take-up Device Thereof	Connector for Prohibiting Gas From Flowing Back	Fixing Device	Method and Circuit for Controlling Switching Frequency Signal	Handle Assembly For Enabling Electrical Product To Work	Colorful-Indexing Portfolio
Filing Date/ Issue Date	Mar. 26, 2001	Apr. 30, 2001	Dec. 10, 2001	Apr. 2, 2001/ May 28, 2002	Oct. 30, 2000/ Oct. 2, 2001	Oct. 15, 2002	Mar. 9, 2001/ Aug. 20, 2002	Nov. 26, 2002
Application No./ Patent No.	09/817,912	09/846,939	10/012,955	09/824,159/ 6,394,121	09/699,692/ 6,297,444	10/271,337	09/803,003/ 6,437,260	10/304,228
New Docket No.	DEEP-1019US	DEEP-1020US	DEEP-1021US	DEEP-1022US	DEEP-1023US	DEEP-1024US	DEEP-1026US	DEEP-1025US
Docket No.	3330	3339	3426	3332	3290	3495	3503	3325